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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): YAMADA et al

Serial No.: 09/767,830

Filed: January 24, 2001

For: Semiconductor Integrated Circuit Device And Process
For Manufacturing The Same

Group: 2811

Examiner:

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

July 30, 2001

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the original specification with the attached Substitute Specification.

IN THE ABSTRACT OF THE DISCLOSURE:

Please replace the original abstract with the attached abstract.